



PATENT

Docket No.: 29287/148

THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS : Kiyokazu NISHIOKA, et al.
SERIAL NO. : 10/053,683
FILED : 24 January 2002
FOR : PROCESSOR INCLUDING A PLURALITY OF COMPUTING DEVICES
GROUP ART UNIT : 2194 Conf. No. 1063
EXAMINER : Van H. Nguyen

MAIL STOP ISSUE FEE - DRAWINGS

COMMISSIONER FOR PATENTS

Customer Service Window

Randolph Bldg.

401 Dulany Street

Alexandria, VA 22314

ATTENTION: DRAWING PROCESSING BRANCH

SUBMISSION OF CORRECTED FIG. 23

SIR:

While the drawings have been approved by the Examiner in the Notice of Allowability mailed 01 June 2005, submitted herewith is a corrected Fig. 23 wherein INSTRUCTION EXPANSION CIRCUIT originally numbered "201" has been re-numbered -241--, as amended in parent application Number 08/913,840. A red-marked sheet showing the correction and a clean corrected formal Fig. 23 are enclosed. Please substitute this sheet for the drawing of Fig. 23 submitted at the time of filing this application.

Respectfully submitted,

KENYON & KENYON


David J. Zibelli
(Reg. No. 36,394)

Dated: August, 31 2005

1500 K Street, N.W., Suite 700
Washington, DC 20005

Tel: (202) 220-4200
Fax: (202) 220-4201